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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10092526	FILING DATE 03/08/2002	CLASS 257	SUBCLASS 312	GAU 2815	EXAMINER Brook
**APPLICANTS: Shigematsu Hisao; Imanishi Kenji; Tanaka Hitoshi;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/191,543 11/13/1998 now U.S. PAT. no. 6399971 <i>ChG</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 10-118466/1998 04/28/1998 <i>yes</i> <i>ChG</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>ChG</i>				981308A	
TITLE : Semiconductor device and method for fabricating the same					
U.S. DEPT. OF COMM. /PAT. & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

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